Roll No. _____

END SEMESTER EXAMINATION
SEMESTER-V

COURSE CODE : CSE 405 | **DURATION : 3 (THREE)HRS**
COURSE TITLE : MICROPROCESSOR | **MAXIMUM MARKS : 75**
SESSION : JULY-DEC'20 21 | **PASS MARKS : 35 %**

NOTE:
1. *All the questions are compulsory*
2. *Attempt any 5 (five) questions.*
3. *Mark for each question is shown against it.*
4. *Assume suitable logical data wherever necessary.*

1.	**(a)**	Define evolution of microprocessor?	**(5)**
	(b)	Write about some specific features of a microprocessor and its daily application	**(5)**
	(c)	Differentiate between a microprocessor and a microcomputer	**(5)**
2.	**(a)**	Define the concept of bus structure.	**(5)**
	(b)	Explain internal architecture of 8085 with the help of diagram.	**(10)**
3.	**(a)**	How instruction cycle is different from machine cycle?	**(5)**
	(b)	Distinguish between general purpose register and specific purpose register?	**(5)**
	(c)	Distinguish between internal and external interrupts	**(5)**
4.	**(a)**	Writes features of 8086 microprocessor	**(5)**
	(b)	Define interfacing. Explain how the I/O devices interfaced with 8085 microprocessor.	**(5)**
	(c)	What are the main differences between assembler and compiler?	**(5)**
5.	**(a)**	List and briefly explain the various types of machine cycles available in 8085	**(5)**
	(b)	Define flags register and briefly explain the different types of flags register.	**(5)**
	(c)	Write the program for addition of two 8 bit numbers. Result is 8 bit.	**(5)**
6.	**(a)**	Describe various addressing modes of 8085 microprocessor with examples	**(5)**
	(b)	Distinguish between synchronous and asynchronous of data transfer	**(5)**
	(c)	How will the multiplexed address/data bus (AD0-AD7) of the 8085 microprocessor be De-multiplexed?	**(5)**
7.	**(a)**	Explain in details with a neat figure the working of the pin details of 8085 microprocessor	**(8)**
	(b)	What are latches? Explain the important of latches in the 8085 microprocessor?	**(7)**
8.	**(a)**	What do you mean by register? Explain all the types of register are available in 8085?	**(8)**
	(b)	Define instruction. Also List and explain the various data transfer instruction	**(7)**

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